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- 1) An adaptive cross-layer fault recovery solution for reconfigurable SoCs: Configuration Layer , Management Layer and External Layer.
- 2) The Fault Detection and Correction Controller and High Speed Configuration Controller implemented as hardware IPs & Fault Recovery Engine including scrubbing, configuration data encoding, dynamic task transfer and checkpoints techniques.
- 3) Evaluated on the Xilinx ML605 Board by the SBU emulation experiment.